



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Jay Sethuram, Richard Weber, and Chandra Shekh
Assignee: Cisco Technology, Inc.
Title: Method And System For Source Synchronous Clocking
Serial No.: 09/450,802 Filing Date: November 29, 1999
Examiner: Duc T. Duong Group Art Unit: 2663
Docket No.: CIS0089US

Austin, Texas
Date Mailed: March 20, 2006

MAIL STOP ISSUE FEE
COMMISSIONER FOR PATENTS
Attention: Official Draftsperson
P. O. Box 1450
Alexandria, VA 22313-1450

SUBMISSION OF FORMAL DRAWINGS

Dear Sir:

Applicants submit three (3) sheets of formal drawings, consisting of Figures 1, 3; 2 and 4, 5 in the above-named application. If there are any questions regarding these drawings, please call the undersigned at (512) 439-5087.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to Mail Stop Issue Fee, Commissioner For Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on March 20, 2006.

Brenna A. Brock 3-20-2006
Attorney for Applicant(s) Date of Signature

Respectfully submitted,

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